

In re Application of:
Shiv Kumar Gupta

DATE: APRIL 29, 2009

Filed: October 15, 2003

Group Art Unit: 2128

For: PARALLEL INSTANCES OF A PLURALITY
OF SYSTEMS ON CHIP IN HARDWARE
EMULATOR VERIFICATION

EXAMINER: GEBRESILASSIE, KIBROM K

CONFIRMATION NO. 6313

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper responds to the Non-Compliant Office Action dated April 17, 2009, and to the Office communication mailed 9/8/08, in the above-identified application. The Applicant respectfully requests consideration of the following remarks.

A Claim Listing begins on page 2 of this paper.

Remarks begin on page 3 of this paper.